

ABSTRACT OF THE DISCLOSURE

An image processing apparatus capable of acquiring a high resolution image without a reduction in sensitivity includes a plurality of sensor chips connected to one another, each sensor chip including a first pixel row and a second pixel row, which are formed on the same semiconductor chip. The first pixel row has a plurality of pixels arranged in the main scanning direction, and the second pixel row has a plurality of pixels shifted along the main scanning direction with respect to the first pixel row.

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